

Notice of References Cited	Application/Control No. 10/777,419		Applicant(s)/Patent Under Reexamination DICK ET AL.	
	Examiner Andre Pierre-Louis		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,389,379	05-2002	Lin et al.	703/14
*	B	US-6,968,514	11-2005	Cooke et al.	716/1
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tabbara et al. (Fast Hardware-Software Co-Simulation Using VHDL Models, 12/4/1998).
	V	Seungjun Lee (A Hardware-Software co-Simulation Environment, PHD Thesis University of California, Berkeley 1993)
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.